

# Resume - Christophe Delord

<b>Personal data</b>	
Christophe Delord	4 rue du Lac d'Oô, 81370 Saint Sulpice, FRANCE web: <a href="https://cdelord.fr">https://cdelord.fr</a> github: <a href="https://github.com/CDSOft">https://github.com/CDSOft</a> LinkedIn: <a href="https://www.linkedin.com/in/cdelord">https://www.linkedin.com/in/cdelord</a> 49 year old – born in 331 PPM
<b>Experience</b>	
Computer science	<b>Computer Science Engineer</b> <b>Post Graduate Degree in Artificial Intelligence</b> <b>ENSEEIH</b> 25 year experience (artificial intelligence, embedded computers, real time, avionics, automotive...)
<b>Technical Skills</b>	
Functional languages Logic languages Imperative/object languages Low level languages Script languages Operating systems Version control Documentation Safety-critical standards	Haskell, OCaml, LISP PROLOG C, Lua, Python, C++ assembly, 80x86, SHARC, PowerPC, PIC32 Bash, Perl, Python, Lua UNIX, GNU/Linux, Debian, Fedora Git Markdown, reStructuredText, Pandoc, LaTeX, HTML DO-178B (avionics), ISO 26262 (automotive)
<b>Experiences - free softwares</b>	
<a href="#">BonaLua</a> , <a href="#">LuaX</a> <a href="#">bang</a> <a href="#">PP</a> , <a href="#">ABP</a> , <a href="#">Panda</a> , <a href="#">UPP</a> , <a href="#">ypp</a> <a href="#">Modelling/simulation</a> <a href="#">Personal web site</a> <a href="#">TPG</a> , <a href="#">SP</a> <a href="#">PyLog</a> <a href="#">PopF</a>	<b>Compact Lua extension</b> - multi platform (GNU/Linux, MacOS and Windows), C and Lua <b>Ninja file generator scriptable in LuaX</b> - Lua <b>Text preprocessor</b> designed for <a href="#">Pandoc</a> , Markdown and reStructuredText written in <a href="#">Haskell</a> and <a href="#">Lua</a> <b>Modeling, simulate and verify critical real time systems</b> with functional languages ( <a href="#">Haskell</a> ) written with Markdown, Pandoc, bang/ninja and LuaX <b>Syntactic parser generators</b> - Python <b>First order logic and PROLOG in Python</b> <b>Unsolicited Emails Filtering</b> - Bayesian filter, POP3 proxy, Python
<b>Patents</b>	
Dec. 20, 2019	Method and system for handling blind sectors of scanning layers of redundant sensors in a vehicle. See <a href="#">patents.google.com</a> or <a href="#">patents.justia.com</a>
<b>Professional Experience</b>	
Feb. 2017 - ...	<b>EasyMile.</b> Real-time embedded software, Sensor and environment simulation (C, Haskell, Lua, Python, Ethernet, CAN, Linux)
Studies	<b>Sopra</b> Usage of functional languages (Haskell, OCaml, F#) to model real time embedded systems Genetic algorithms applied to automatic unit test generation
Aug. 2015 - Jan. 2017	<b>Sopra</b> : real time simulation Airbus: real time simulation for flight computers integrated to the global A380 simulator (Simics, Power PC, Linux, AFDX)
Sept. 2014 - Jan. 2017	<b>Sopra</b> : Flight tests Airbus: Wi-Fi network optimisation, Real time Linux OS, update of the acquisition and analyzing system of the flight recorded data
Feb. 2014	<b>Sopra</b> : Experimentation with Microchip PIC32
Jan. 2015 - June 2015	Airbus: study of a real-time architecture for flight test data acquisition modules (PIC32, clock synchronization) Sopra Spain, Fermax (Valencia): Feasibility study of a VoIP intercom
Oct. 2013 - Mar. 2014	<b>Sopra</b> : Qualified ARINC 665 load generator Thales Avionics: Design and code in C, Generic data forming system
Sept. 2012 - Aug. 2014	<b>Sopra</b> : Real-time modular test bench (configurable by Python scripts) Thales Optronique: design, code and tests. Real-time kernel in C++ (Windows, RTX), generic I/O modules, configuration and behaviour of the kernel and modules in Python (embedded interpreter)
Apr. 2012 - Oct. 2012	<b>Sopra</b> : Onboard Maintenance System (OMS) simulator, DO-178B, Python Liebherr Aerospace: design, code and test of an OMS (ARINC 604 simulator in Python, ARINC 429 interface), Python scriptable test environment, LRU simulation for validation purpose, automatic documentation generation in Python and reStructuredText (Sphinx, test results, traceability)
May 2001 - June 2014	<b>Sopra</b> : real-time embedded software, DO-178B Liebherr Aerospace: assisted unit test generation in Python for RTRT Thales Avionics: A320 Flight Control computer, specification, design, code, tests Airbus: A380 and A320 Flight Control computer, specification, design, code, tests (France, training of an Indian team) Airbus: microprocessor simulation (Python, graph, WCET computation, stack analyzer) Airbus: safety studies
Oct. 1998 - May 2001	<b>Sopra</b> CNRS, Pierre Fabre Laboratories: databases
<b>Education</b>	
1997 - 1998	<b>ENSEEIH - IRIT</b> : Post Graduate Degree in Artificial Intelligence Publication: Speech acts and dialog games (Colloque Intelligence Artificielle et Complexité, Université Saint Denis, Paris VIII)
1995 - 1998	<b>ENSEEIH</b> : Computer Science Engineer
<b>Langues</b>	
French English	Native Speaker Intermediate
<b>Links</b>	
Full resume	HTML: <a href="http://cdelord.fr/cv/cv.fr.html">http://cdelord.fr/cv/cv.fr.html</a> - PDF: <a href="http://cdelord.fr/cv/cv.fr.pdf">http://cdelord.fr/cv/cv.fr.pdf</a>